



CMLD6263A  
CMLD6263C  
CMLD6263S

**SURFACE MOUNT PICOmini™  
DUAL PAIR, HIGH VOLTAGE  
SILICON SCHOTTKY DIODES**

**PICOmini™**



**SOT-563 CASE**

# Central™

## Semiconductor Corp.

### DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLD6263 Series incorporates two pair, high voltage, low  $V_F$  Silicon Diodes in a space saving SOT-563 surface mount package. These diodes are designed for fast switching applications requiring a low forward voltage drop.

### MARKING CODES:

**CMLD6263A: 63A**  
**CMLD6263C: 63C**  
**CMLD6263S: 63S**

### FEATURES:

- High Voltage (70V)
- Low Forward Voltage
- Various Dual Pair Configurations

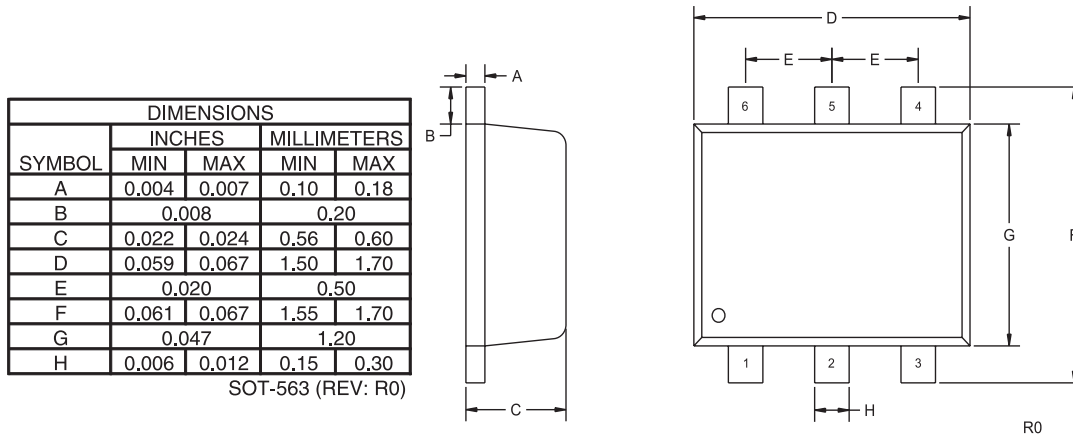
### MAXIMUM RATINGS: ( $T_A=25^\circ\text{C}$ )

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	$V_{RRM}$	70	V
Continuous Forward Current	$I_F$	15	mA
Forward Surge Current, $t_p=1.0\text{s}$	$I_{FSM}$	50	mA
Power Dissipation	$P_D$	250	mW
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$
Thermal Resistance	$\Theta_{JA}$	500	$^\circ\text{C/W}$

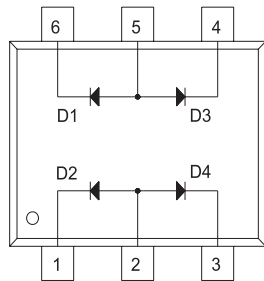
### ELECTRICAL CHARACTERISTICS PER DIODE: ( $T_A=25^\circ\text{C}$ )

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$I_R$	$V_R=50\text{V}$		98	200	nA
$BV_R$	$I_R=10\mu\text{A}$	70			V
$V_F$	$I_F=1.0\text{mA}$		395	410	mV
$C_T$	$V_R=0\text{V}, f=1.0\text{MHz}$			2.0	pF
$t_{tr}$	$I_R=I_F=10\text{mA}, I_{rr}=1.0\text{mA}, R_L=100\Omega$			5.0	ns

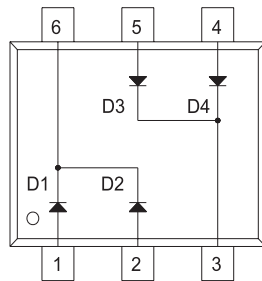
**SOT-563 CASE - MECHANICAL OUTLINE**



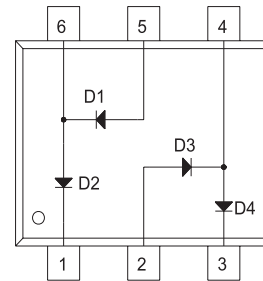
**PIN CONFIGURATIONS - DUAL PAIR**



**CMLD6263A**  
**COMMON ANODE**  
**MARKING CODE: 63A**  
**LEAD CODE:**  
1) Cathode D2  
2) Anode D2, D4  
3) Cathode D4  
4) Cathode D3  
5) Anode D1, D3  
6) Cathode D1



**CMLD6263C**  
**COMMON CATHODE**  
**MARKING CODE: 63C**  
**LEAD CODE:**  
1) Anode D1  
2) Anode D2  
3) Cathode D3, D4  
4) Anode D4  
5) Anode D3  
6) Cathode D1, D2



**CMLD6263S**  
**IN SERIES**  
**MARKING CODE: 63S**  
**LEAD CODE:**  
1) Cathode D2  
2) Anode D3  
3) Cathode D4  
4) Cathode D3, Anode D4  
5) Anode D1  
6) Cathode D1, Anode D2